

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Cosmin Iorga
Title: HIGH-SPEED DIGITAL MULTIPLEXER
Patent. No.: 7,222,041
Issue Date: May 22, 2007
Examiner: Anthony Gutierrez
Art Unit: 2857
Confirmation Number: 6760

**REQUEST FOR CERTIFICATE OF CORRECTION FOR
PTO MISTAKE PURSUANT TO 37 C.F.R. § 1.322(a)**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Enclosed, in duplicate, is a Certificate of Correction, Form PTO-1050, for United States Patent Number 7,222,041 issued May 22, 2007. The following Patent Office printing errors appear in the issued patent:

IN THE CLAIMS

The exact claim and line number where the errors in the issued patent are shown correctly in the application file are:

1. At Column 5, Line 19, Claim 5 currently reads as follows:

"A high-speed digital multiplexer according to clam 1"

2. At Column 5, Line 19, please correct Claim 5 to read as follows:

-- A high-speed digital multiplexer according to claim 1—

3. At Column 5, Line 37, Claim 7 currently reads as follows:

“common voltage th branch connected to the common”

4. At Column 5, Line 37, please correct Claim 7 to read as follows:

-- common voltage the branch connected to the common –

5. At Column 5, Line 39, Claim 7 currently reads as follows:

“signal received by art enabled input pin”

6. At Column 5, Line 39, please correct claim 7 to read as follows:

-- signal received by an enabled input pin –

7. At Column 5, Line 45, Claim 7 currently reads as follows:

“selected one of plurality of input pins based on the”

8. At Column 5, Line 45, please correct claim 7 to read as follows:

-- selected one of the plurality of input pins based on the –

9. At Column 6, Line 6, Claim 12 currently reads as follows:

“A high-speed digital multiplexer according to claim 7”

10. At Column 6, Line 6, please correct Claim 12 to read as follows:

-- A high-speed digital multiplexer according to claim 5 –

11. At Column 6, Line 8, Claim 12 currently reads as follows:

“and at least one voltage of the non-selected ones of he”

12. At Column 6, Line 8, please correct Claim 12 to read as follows:
-- and at least one voltage of the non-selected ones of the --

13. At Column 6, Line 52, Claim 17 currently reads as follows:
“voltage then non-selected ones of the plurality of input pins.”

14. At Column 6, Line 52, please correct Claim 17 to read as follows:
-- voltage than non-selected ones of the plurality of input pins. --

15. At Column 6, Line 54, Claim 18 currently reads as follows:
“wherein the common voltage is higher then the first voltage”

16. At Column 6, Line 54, please correct Claim 18 to read as follows:
-- wherein the common voltage is higher than the first voltage --

Applicant submits that the above changes would not constitute new matter, and correction thereof would not require reexamination.

Pursuant to 37 C.F.R. §1.322, Applicant requests that the enclosed Certificate of Correction be approved.

Although Applicant believes that no fee is required for this Request, the Commissioner is hereby authorized to charge any additional fees which may be required for this Request to Deposit Account No. 19-0741.

Respectfully submitted,

Date June 22, 2007

FOLEY & LARDNER LLP
111 Huntington Avenue
Boston, Massachusetts 02199
Telephone: (617) 342-4034
Facsimile: (617) 342-4001

By _____



David J. Rikkers
Attorney for Applicant
Registration No. 43,882

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO. : 7,222,041

APPLICATION NO. : 10/010,547

DATED : May 22, 2007

INVENTOR(S) : Cosmin Iorga

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

1. At Column 5, Line 19, Claim 5 currently reads as follows:
"A high-speed digital multiplexer according to claim 1"
2. At Column 5, Line 19, please correct Claim 5 to read as follows:
-- A high-speed digital multiplexer according to claim 1—
3. At Column 5, Line 37, Claim 7 currently reads as follows:
"common voltage th branch connected to the common"
4. At Column 5, Line 37, please correct Claim 7 to read as follows:
-- common voltage the branch connected to the common —
5. At Column 5, Line 39, Claim 7 currently reads as follows:
"signal received by art enabled input pin"
6. At Column 5, Line 39, please correct claim 7 to read as follows:
-- signal received by an enabled input pin —
7. At Column 5, Line 45, Claim 7 currently reads as follows:
"selected one of plurality of input pins based on the"
8. At Column 5, Line 45, please correct claim 7 to read as follows:
-- selected one of the plurality of input pins based on the —

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Foley & Lardner LLP
111 Huntington Avenue, 26th Floor
Boston, Massachusetts 02199

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 0.1 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer,

U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO. : 7,222,041
APPLICATION NO. : 10/010,547
DATED : May 22, 2007
INVENTOR(S) : Cosmin Iorga

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

9. At Column 6, Line 6, Claim 12 currently reads as follows:
"A high-speed digital multiplexer according to claim 7"

10. At Column 6, Line 6, please correct Claim 12 to read as follows:
-- A high-speed digital multiplexer according to claim 5 --

11. At Column 6, Line 8, Claim 12 currently reads as follows:
"and at least one voltage of the non-selected ones of he"

12. At Column 6, Line 8, please correct Claim 12 to read as follows:
-- and at least one voltage of the non-selected ones of the --

13. At Column 6, Line 52, Claim 17 currently reads as follows:
"voltage then non-selected ones of the plurality of input pins."

14. At Column 6, Line 52, please correct Claim 17 to read as follows:
-- voltage than non-selected ones of the plurality of input pins. --

15. At Column 6, Line 54, Claim 18 currently reads as follows:
"wherein the common voltage is higher then the first voltage"

16. At Column 6, Line 54, please correct Claim 18 to read as follows:
-- wherein the common voltage is higher than the first voltage --

MAILING ADDRESS OF SENDER (Please do not use customer number below).

Foley & Lardner LLP
111 Huntington Avenue, 26th Floor
Boston, Massachusetts 02199

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer.

U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO. : 7,222,041
APPLICATION NO. : 10/010,547
DATED : May 22, 2007
INVENTOR(S) : Cosmin Iorga

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

1. At Column 5, Line 19, Claim 5 currently reads as follows:
"A high-speed digital multiplexer according to claim 1"
2. At Column 5, Line 19, please correct Claim 5 to read as follows:
-- A high-speed digital multiplexer according to claim 1 --
3. At Column 5, Line 37, Claim 7 currently reads as follows:
"common voltage th branch connected to the common"
4. At Column 5, Line 37, please correct Claim 7 to read as follows:
-- common voltage the branch connected to the common --
5. At Column 5, Line 39, Claim 7 currently reads as follows:
"signal received by art enabled input pin"
6. At Column 5, Line 39, please correct claim 7 to read as follows:
-- signal received by an enabled input pin --
7. At Column 5, Line 45, Claim 7 currently reads as follows:
"selected one of plurality of input pins based on the"
8. At Column 5, Line 45, please correct claim 7 to read as follows:
-- selected one of the plurality of input pins based on the --

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Foley & Lardner LLP
111 Huntington Avenue, 26th Floor
Boston, Massachusetts 02199

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 0.1 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO. : 7,222,041
APPLICATION NO. : 10/010,547
DATED : May 22, 2007
INVENTOR(S) : Cosmin Iorga

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

9. At Column 6, Line 6, Claim 12 currently reads as follows:
"A high-speed digital multiplexer according to claim 7"

10. At Column 6, Line 6, please correct Claim 12 to read as follows:
-- A high-speed digital multiplexer according to claim 5 --

11. At Column 6, Line 8, Claim 12 currently reads as follows:
"and at least one voltage of the non-selected ones of he"

12. At Column 6, Line 8, please correct Claim 12 to read as follows:
-- and at least one voltage of the non-selected ones of the --

13. At Column 6, Line 52, Claim 17 currently reads as follows:
"voltage then non-selected ones of the plurality of input pins."

14. At Column 6, Line 52, please correct Claim 17 to read as follows:
-- voltage than non-selected ones of the plurality of input pins. --

15. At Column 6, Line 54, Claim 18 currently reads as follows:
"wherein the common voltage is higher then the first voltage"

16. At Column 6, Line 54, please correct Claim 18 to read as follows:
-- wherein the common voltage is higher than the first voltage --

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Foley & Lardner LLP
111 Huntington Avenue, 26th Floor
Boston, Massachusetts 02199

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (or by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour per application (including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer.

U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: **Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.